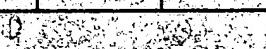


		Subclass			C		PATENT NUMBER
		Class					
		ISSUE CLASSIFICATION					
<b>U.S. UTILITY Patent Application</b>							
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REV.	DOE	SCANNED	8/3	Q.A.	<i>S. B.</i>		
PTO-1305	NASA US COMM-DC						
APPLICATION NO. 09/731726	CONT/PRIOR F	CLASS 257	SUBCLASS <i>148</i>	ART UNIT 2811	EXAMINER <i>Jp</i>		
APPLICANTS Kozo Shimizu Masayuki Ochiai Yasuo Yamagishi							
<b>BEST AVAILABLE COPY</b>							
Solder alloy, a circuit substrate, a semiconductor device and a method of manufacturing the same							
TITLE	PTO-2040 12/89						

## **ISSUING CLASSIFICATION**

Continued on Issue Slip Inside File Jacket

<b>TERMINAL DISCLAIMER</b>		<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
		<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print Fig.</b>	<b>Total Claims</b>	<b>Print Claim for O.G.</b>
<input type="checkbox"/> The term of this patent subsequent to _____ has been disclaimed. (date)		 (Assistant Examiner) _____ (Date) _____			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____		 (Primary Examiner) _____ (Date) _____			<b>ISSUE FEE</b>	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.		 (Legal Instruments Examiner) _____ (Date) _____			<b>Amount Due</b>	<b>Date Paid</b>
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